IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No	10/648.886
Confirmation No	
Filing Date	
Inventorship	
Assignee	
Group Art Unit	
Examiner	O. Nadav
Attorney's Docket No	MI22-2374
Customer No	
Title: Integrated Circuitry and a Semiconduc Series of Conductive Lines	

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References - See Attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

This Supplemental Information Disclosure Statement is being filed together with the filing of a Request for Continued Examination. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. § 1.17(p) to Deposit Account No. 23-0925.

Respectfully submitted,

Dated: _	23 Oct 2002	By:
		James∖E, Lake Reg. No. 44,854

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. Sheet 1 of 1

Form PTO-1449			U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			ATTY, DOCKET NO. MI22-2374			SERIAL NO. 10/648,886			
!		LIST OF ART CITES (Use several sheet	OF ART CITED BY APPLICANT (Use several sheets if necessary)			APPLICANT: H. Montgomery Manning						
·			FILING DATE August 26, 2003			GROUP 2811						
U.S. PATENT DOCUMENTS												
"Examiner's Initials		Document Number	Dete	Name		Class Subclass			Filing Date If Appropriate			
		5,858,865	01/1999	Juengling et al.								
	AB	5,378,646	01/1995	Muang et al.								
	AC											
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OTHER RE	FEREN	ICES (including Author	Title, Date, Pe	rtinent Pages, Etc.)								
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.												